



S. S. EDUCATION TRUST'S
S.G. BALEKUNDRI INSTITUTE OF TECHNOLOGY, BELAGAVI
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SGBIT/ACA/02-R2

TIME-TABLE

CLASS: V (A) SEMESTER

ROOM. NO.: B-202

ACADEMIC YEAR: 2021-22

W.E.F: 25-10-2021

DAY/TIME	9:15-10.15am	10.15-11.15am	11.15-11.30am	11.30-12.30pm	12.30-1.30pm	1:30-2:30pm	2.30-3.20pm	3.20-4.10pm	4.10-5.00pm	BATCH	USN
MONDAY	DSP	EMW	BREAK	PCS	M&E	LUNCH BREAK	DSPL-A1/HDLL-A2		A ₁	2BU18EC027 TO 2BU19EC024	
TUESDAY	M&E	PCS		ITC	DSP		DSPL-A2/HDLL-A1				
WEDNESDAY	ITC	DSP		EMW	PCS		Arduino Lab practice		A ₂	2BU18EC025 TO 2BU19EC089	
THURSDAY	HDL	M&E		DSP	PLACEMENT		EVS				
FRIDAY	HDL	PCS		ITC	DSP		Mini Project				
SATURDAY	EMW	HDL		PCS							

SUBJECT CODE	SUBJECT NAME	SUBJECT INITIALS	FACULTY NAME	HOURS/WEEK (L+T+P)	HOURS/WEEK AS PER VTU SCHEME (L+T+P)	TECH. STAFF NAME (For Labs)
18ES51	TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	M&E	PROF. KIRAN NANDI	3+0+0	3+0+0	
18EC52	DIGITAL SIGNAL PROCESSING	DSP	PROF. SUJAY GEJJI	3+2+0	3+2+0	
18EC53	PRINCIPLES OF COMMUNICATION SYSTEMS	PCS	PROF. JAGADISH JAKATI	3+2+0	3+2+0	
18EC54	INFORMATION THEORY & CODING	ITC	PROF. SHILPA BHAIRANATTI	3+0+0	3+0+0	
18EC55	ELECTROMAGNETIC WAVES	EMW	DR. SRIDHAR IYER	3+0+0	3+0+0	
18EC56	VERILOG HDL	HDL	PROF. VINAYAK HONNUNGAR	3+0+0	3+0+0	
18ECL57	DIGITAL SIGNAL PROCESSING LABORATORY	DSPL	PROF. SUJAY GEJJI	0+2+2	0+2+2	Mr.prateek.B
18ECL58	HDL LABORATORY	HDLL	PROF. RAMACHANDRA T.	0+2+2	0+2+2	Ms.sangeeta
18CIV59	ENVIRONMENTAL STUDIES	EVS	PROF. AKSHATA	1+0+0	1+0+0	

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