



S. S. EDUCATION TRUST'S
S.G. BALEKUNDRI INSTITUTE OF TECHNOLOGY, BELAGAVI
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SGBIT/ACA/02-R2

CLASS: V (B) SEMESTER
ACADEMIC YEAR: 2021-22

TIME-TABLE

ROOM. NO.: B-203
W.E.F: 25-10-2021

DAY/TIME	9:15-10.15am	10.15-11.15am	11.15-11.30am	11.30-12.30pm	12.30-1.30pm	1:30-2:30pm	2.30-3.20 pm	3.20-4.10 pm	4.10-5.00 pm	BATCH	USN
MONDAY	DSP	M&E	BREAK	HDL	PCS	LUNCH BREAK	Mini Project			B ₁	2BU19EC004 TO 2BU19EC068
TUESDAY	ITC	HDL		EMW	DSP		PCS(T)				
WEDNESDAY	HDL	DSP		ITC	M&E		PCS(T)				
THURSDAY	EMW	ITC		DSP	PLACEMENT		EVS				
FRIDAY	PCS	DSPL-B2/HDLL-B1					Arduino Lab practice			B ₂	2BU19EC069 TO 2BU20EC406
SATURDAY	M&E	PCS	BREAK	DSP	EMW		DSPL-B1/HDLL-B2				

SUBJECT CODE	SUBJECT NAME	SUBJECT INITIALS	FACULTY NAME	HOURS/WEEK (L+T+P)	HOURS/WEEK AS PER VTU SCHEME (L+T+P)	TECH. STAFF NAME (For Labs)
18ES51	TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	M&E	PROF. ANITA PATIL	3+0+0	3+0+0	
18EC52	DIGITAL SIGNAL PROCESSING	DSP	PROF. SHANKARGOUDA P	3+2+0	3+2+0	
18EC53	PRINCIPLES OF COMMUNICATION SYSTEMS	PCS	PROF. SIDRAMAYYA M	3+2+0	3+2+0	
18EC54	INFORMATION THEORY & CODING	ITC	PROF. VEENA I P	3+0+0	3+0+0	
18EC55	ELECTROMAGNETIC WAVES	EMW	PROF. NARAYAN A B	3+0+0	3+0+0	
18EC56	VERILOG HDL	HDL	PROF. MAHADEVAPPA M	3+0+0	3+0+0	
18ECL57	DIGITAL SIGNAL PROCESSING LABORATORY	DSPL	PROF. SHANKARGOUDA P	0+2+2	0+2+2	Mr.Prateek.B.
18ECL58	HDL LABORATORY	HDLL	PROF. MAHADEVAPPA M	0+2+2	0+2+2	Ms.Sangeeta
18CIV59	ENVIRONMENTAL STUDIES	EVS	PROF. AKSHATA	1+0+0	1+0+0	

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PROF. SHILPA B
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