



**S. S. EDUCATION TRUST'S  
S.G. BALEKUNDRI INSTITUTE OF TECHNOLOGY, BELAGAVI  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**SGBIT/ACA/02-R2**

**CLASS: VII (A) SEMESTER**

**ACADEMIC YEAR: 2021-22**

**TIME-TABLE**

**ROOM. NO.: B-204**

**W.E.F: 25-10-2021**

DAY/TIME	9:15-10.15am	10.15-11.15am	11.15-11.30am	11.30-12.30pm	12.30-1.30pm	1:30-2:30pm	2.30-3.20 pm	3.20-4.10 pm	4.10-5.00 pm	BATCH	USN
MONDAY	CN	VLSI	BREAK	PE4	PROJECT	LUNCH BREAK				A <sub>1</sub>	2BU18EC001 TO 2BU18EC030
TUESDAY	VLSI	RTS		CN	PROJECT						
WEDNESDAY	CN	OE(NN)		VLSIL(T)	RTS					A <sub>2</sub>	2BU18EC031 TO 2BU19EC420
THURSDAY	PE4	OE(NN)		RTS	PLACEMEN T						
FRIDAY	PE4	OE(NN)		VLSI	CNL(T)		CNL-A1/VLSLI-A2				
SATURDAY	CN-A2/VLSIL-A1				PROJECT						

SUBJECT CODE	SUBJECT NAME	SUBJECT INITIALS	FACULTY NAME	HOURS/WEEK (L+T+P)	HOURS/WEEK AS PER VTU SCHEME (L+T+P)	TECH. STAFF NAME (For Labs)
18EC71	COMPUTER NETWORKS	CN	PROF. VEENA I. PURANIKMATH	3+0+0	3+0+0	
18EC72	VLSI DESIGN	VLSI	PROF. VINAYAK HONNUNGAR	3+0+0	3+0+0	
18EC731	REAL TIME SYSTEMS	RTS	DR. SRIDHAR IYER	3+0+0	3+0+0	
18EC741/ 18EC742/ 18EC745	IOT & WIRELESS SENSOR NETWORKS/AUTOMOTIVE ELECTRONICS/MACHINE LEARNING	PE4	PROF. SHILPA B/PROF. SHANKARGOUDA P/PROF. SUJAY GEJJI	3+0+0	3+0+0	
18EC742	NEURAL NETWORKS	NN	DR. UDAY WALI	3+0+0	3+0+0	
18ECL76	COMPUTER NETWORK LAB	CNL	PROF. VEENA I. PURANIKMATH	0+2+2	0+2+2	Mrs. Mrunal
18ECL77	VLSI LAB	VLSIL	PROF. RAMACHANDRA T.	0+2+2	0+2+2	Mr. Vijay

**CLASS TEACHER- DR. SHILPA MAYANNAVAR**

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