



S. S. EDUCATION TRUST'S
S.G. BALEKUNDRI INSTITUTE OF TECHNOLOGY, BELAGAVI
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SGBIT/ACA/02-R2

CLASS: VII (A&B) SEMESTER (2015,17 scheme)

ACADEMIC YEAR: 2021-22

TIME-TABLE

ROOM NO.: ARDIUNO LAB

W.E.F: 25-10-2021

DAY/TIME	9:15-10.15am	10.15-11.15am	11.15-11.30am	11.30-12.30pm	12.30-1.30pm	1:30-2:30pm	2.30-3.20 pm	3.20-4.10 pm	4.10-5.00 pm	BATCH	USN
MONDAY	RTS	M&A	BREAK	DIP	SATCOM	LUNCH BREAK	ADCL			C1	2BU15EC024 TO 2BU18EC410
TUESDAY	RTS	DIP		SATCOM	SATCOM		VLSIL				
WEDNESDAY	DIP	M&A		PE	RTS						
THURSDAY	PE	DIP		M&A	PLACEMENT						
FRIDAY	PE	PE		M&A	PROJECT						
SATURDAY	PROJECT	PROJECT		PROJECT	PROJECT						

SUBJECT CODE	SUBJECT NAME	SUBJECT INITIALS	FACULTY NAME	HOURS/WEEK (L+T+P)	HOURS/WEEK AS PER VTU SCHEME (L+T+P)	TECH. STAFF NAME (For Labs)
15/17EC71	MICROWAVE AND ANTENNAS	M&A	PROF. JAGADISH JAKATI	4+0+0	4+0+0	
15/17EC72	DIGITAL IMAGE PROCESSING	DIP	PROF. MEGHA K	4+0+0	4+0+0	
15/17EC73	POWER ELECTRONICS	PE	PROF. ANANDRADDY N	4+0+0	4+0+0	
15/17EC743	REAL TIME SYSTEMS	RTS	DR. SIDRAMAYYA MATAD	3+0+0	3+0+0	
15/17EC755	SATELLITE COMMUNICATION	SATCOM	PROF. VINAYAK HONNUNGAR	3+0+0	3+0+0	
15/17ECL76	ADVANCED COMMUNICATION LAB	ADCL	PROF. SIDRAMAYYA MATAD	0+1+2	0+1+2	Ms.Mrunal
15/17ECL77	VLSI LAB	VLSIL	PROF. ANITA PATIL	0+1+2	0+1+2	Mr.Vijay M

Class Teacher- Prof. Megha K

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