



S. S. EDUCATION TRUST'S
S.G. DALEKUNDRI INSTITUTE OF TECHNOLOGY, BELAGAVI
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

SGBIT/ACA/02-R2

CLASS: V (A) SEMESTER

ACADEMIC YEAR: 2022-23

TIME-TABLE

ROOM. NO.: B-205

W.E.F: 10/10/2022

DAY/TIME	9:15-10.15a m	10.15-11.15a m	11.15-11.30am	11.30-12.30pm	12.30-1.30pm	1:30-2:30p m	2.30-3.20 pm	3.20-4.10 pm	4.10-5.00 pm	BATCH	USN
MONDAY	DSP	M&E	BREAK	PCS	EMW	LUNCH BREAK	DSPL-A1/HDLL-A2		A ₁	2BU20EC001 TO 2BU20EC027	
TUESDAY	PCS	HDL		ITC	DSP						
WEDNESDAY	M&E	DSP		PCS(T)	PCS(T)		DSPL-A2/HDLL-A1		A ₂	2BU20EC028 TO 2BU21EC413	
THURSDAY	HDL	ITC		DSP(T)	EMW						
FRIDAY	DSP(T)	PCS		M&E	EVS		Mini(Hobby) Project				
SATURDAY	EMW	HDL		ITC	PLACEMENT						

SUBJECT CODE	SUBJECT NAME	SUBJECT INITIALS	FACULTY NAME	HOURS/WEEK (L+T+P)	HOURS/WEEK AS PER VTU SCHEME (L+T+P)	TECH. STAFF NAME (For Labs)
18ES51	TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	M&E	Mr. KIRAN NANDI	3+0+0	3+0+0	
18EC52	DIGITAL SIGNAL PROCESSING	DSP	Mr. SHANKARAGOUDA PATIL	3+2+0	3+2+0	
18EC53	PRINCIPLES OF COMMUNICATION SYSTEMS	PCS	DR. SURESH B. AKKOLE	3+2+0	3+2+0	
18EC54	INFORMATION THEORY AND CODING	ITC	Mrs. SHILPA BHAIRNATTI	3+0+0	3+0+0	
18EC55	ELECTROMAGNETIC WAVES	EMW	Mr. NARAYAN A. BADIGER	3+0+0	3+0+0	
18EC56	VERILOG HDL	HDL	Ms. MEGHA KARABASHETTI	3+0+0	3+0+0	
18ECL57	DIGITAL SIGNAL PROCESSING LABORATORY	DSPL	Mr. SHANKARAGOUDA PATIL	0+2+2	0+2+2	MR. PRATIK B.
18ECL58	HDL LABORATORY	HDLL	Ms. MEGHA KARABASHETTI/ Mrs. SHILPA BHAIRNATTI	0+2+2	0+2+2	MR. VIJAY M.
18CIV59	ENVIRONMENTAL STUDIES	EVS		1+0+0	1+0+0	

CLASS TEACHER- Ms. MEGHA KARABASHETTI

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[Signature]
 10/10/2022
 PROF. NARAYAN A. BADIGER
 DEPT. ACA. COORDINATOR

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 10/10/22
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 DR. ASHOK M. HULAGABALI
 ACADEMIC COORDINATOR

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 DR. B. R. PATAGUNDI
 PRINCIPAL
 SGBIT, Belagavi